|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  | IO/Mem | | |  | Chip Select | | | Registro | | | | |
|  |  |  | 31 | 30 | 29 | (…) | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| UART0 | TX/RX | 0XE0000000 | 1 | 1 | 1 |  | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| DIVIDER/FLAGS | 0xE0000004 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| UART1 | UART1 TX/RX | 0XE0000008 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| DIVIDER/FLAGS | 0xE000000C | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 |
| UART2 | TX/RX | 0XE0000010 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| DIVIDER/FLAGS | 0xE0000014 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
| SPI | TXDATA/RXDATA | 0xE0000020 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 |
| CONTROL/FLAGS | 0xE0000024 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| SS (SLAVE SELECT) | 0xE0000028 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| I2C | DATA/CONTROL DATA/STATUS | 0xE0000040 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| DIVIDER | 0xE0000044 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| TEMP | MAXCOUNT/TIMER | 0XE0000060 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| GP | GPOUT/GPOUT | 0xE0000080 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| GPOUT/GPIN | 0xE0000084 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
|  | INTERRUPT ENABLE | 0xE00000C0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| IRQ | VECTOR 0: Trap | 0xE00000E0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 |
| VECTOR 1: RX | 0xE00000E4 | 1 | 1 | 1 | 0 | 0 | 1 | 0 | 0 |
| VECTOR 2: TX | 0xE00000E8 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 |
| VECTOR 3: TIMER | 0xE00000EC | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 0 |
| VECTOR 4: RX1 | 0xE00000F0 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| VECTOR 5: TX1 | 0xE00000F4 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
| VECTOR 6: RX2 | 0xE00000F8 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 |
| VECTOR 7: TX2 | 0xE00000FC | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 |

IRAMCS: IO/MEM = 000

IOCS: IO/MEM = 111